

Fig. 1

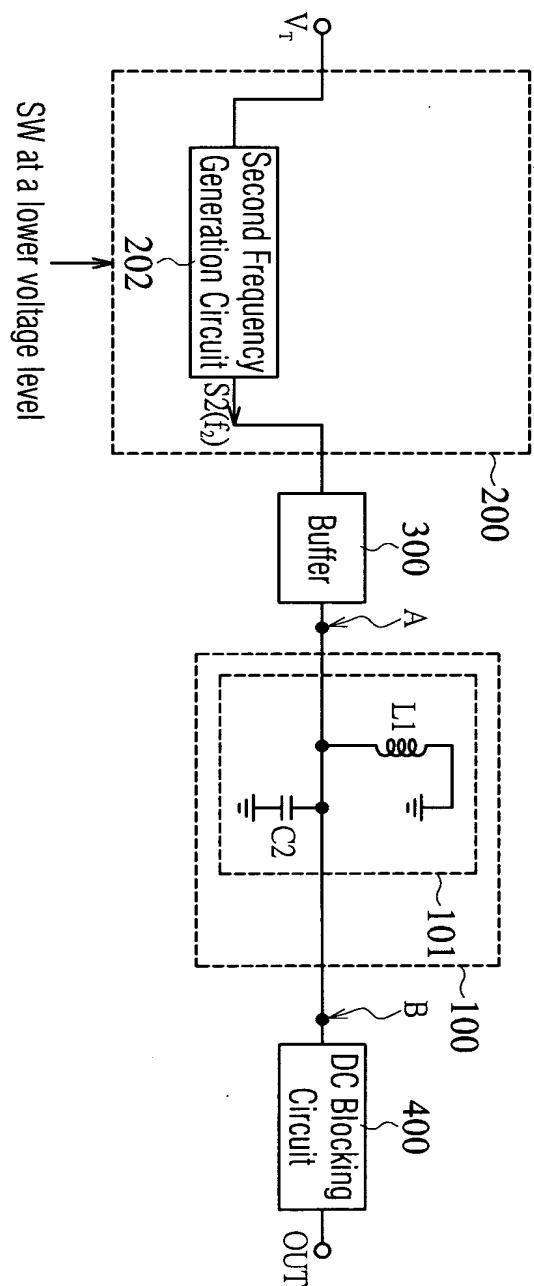


Fig. 2(a)

SW at a lower voltage level

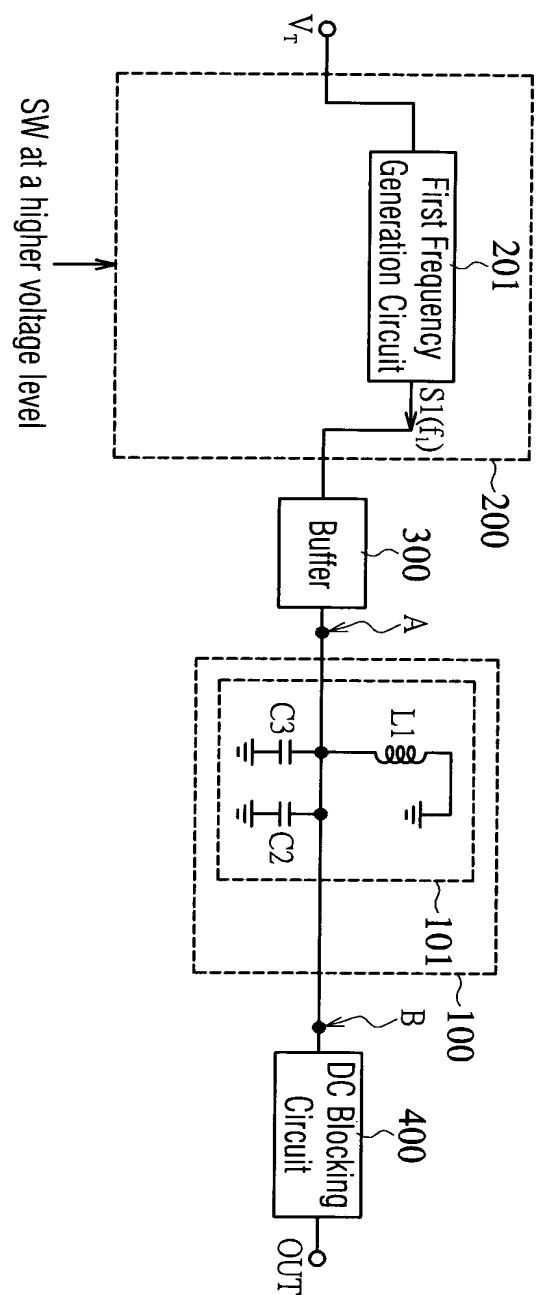


Fig. 2(b)

Fig. 3(b)

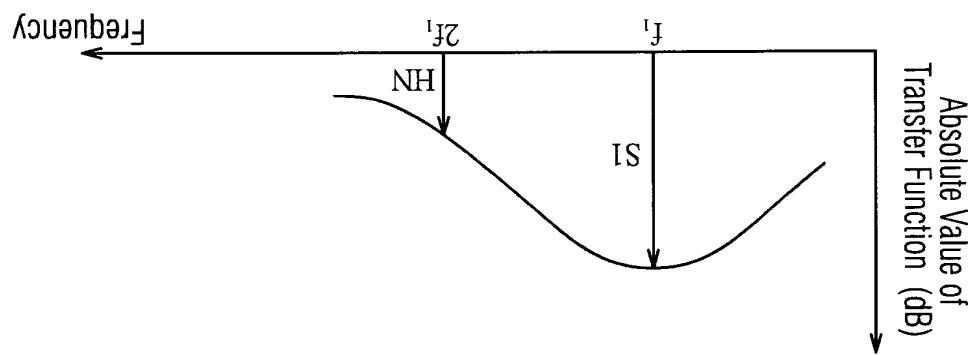


Fig. 3(a)

